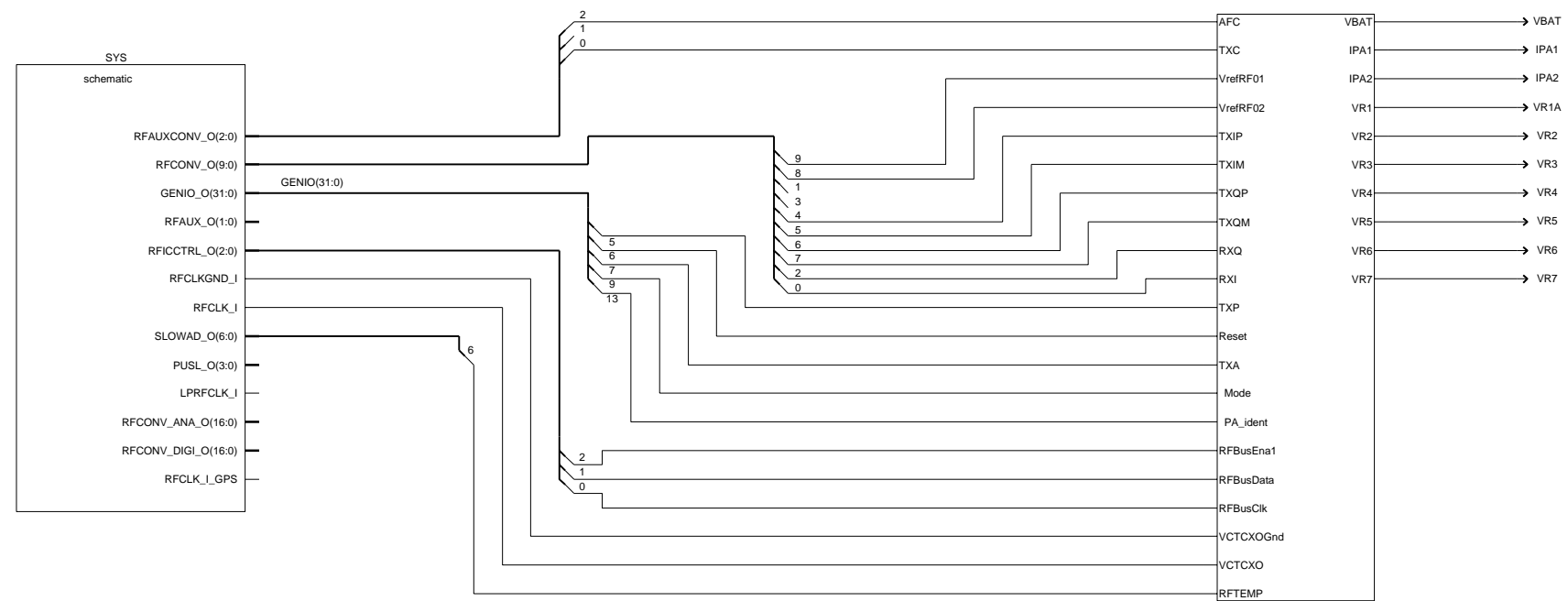
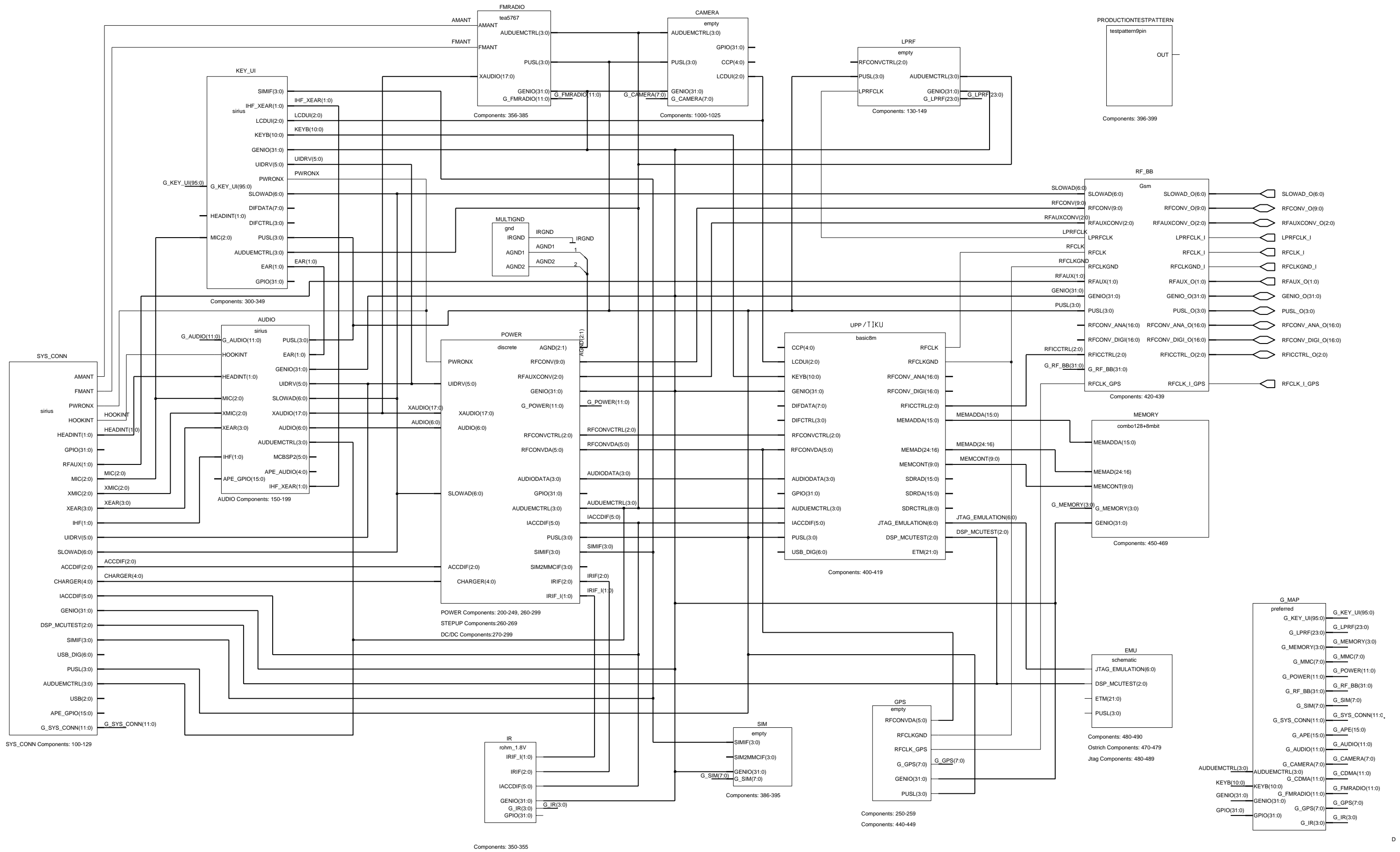


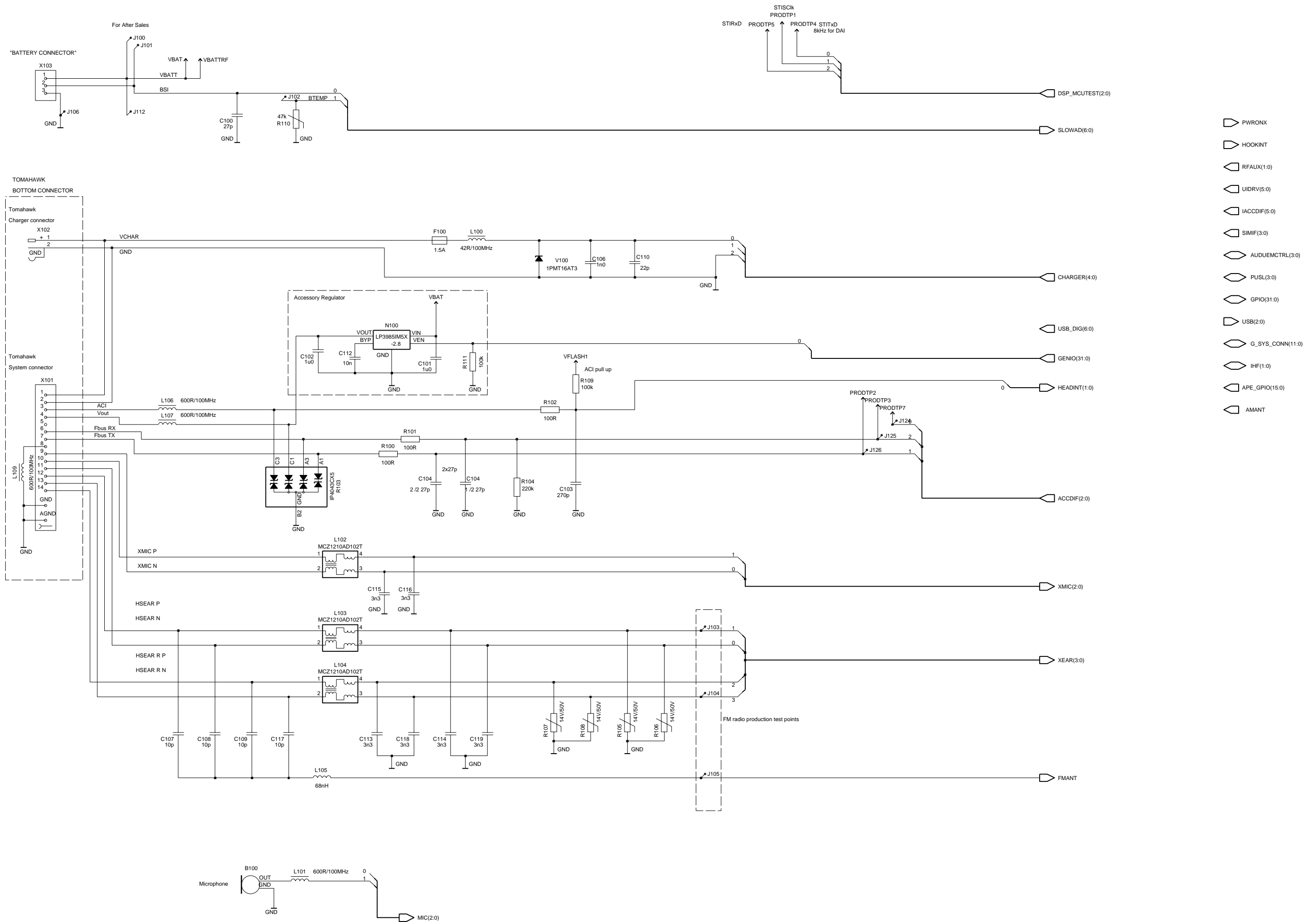
Top Level



Common BB

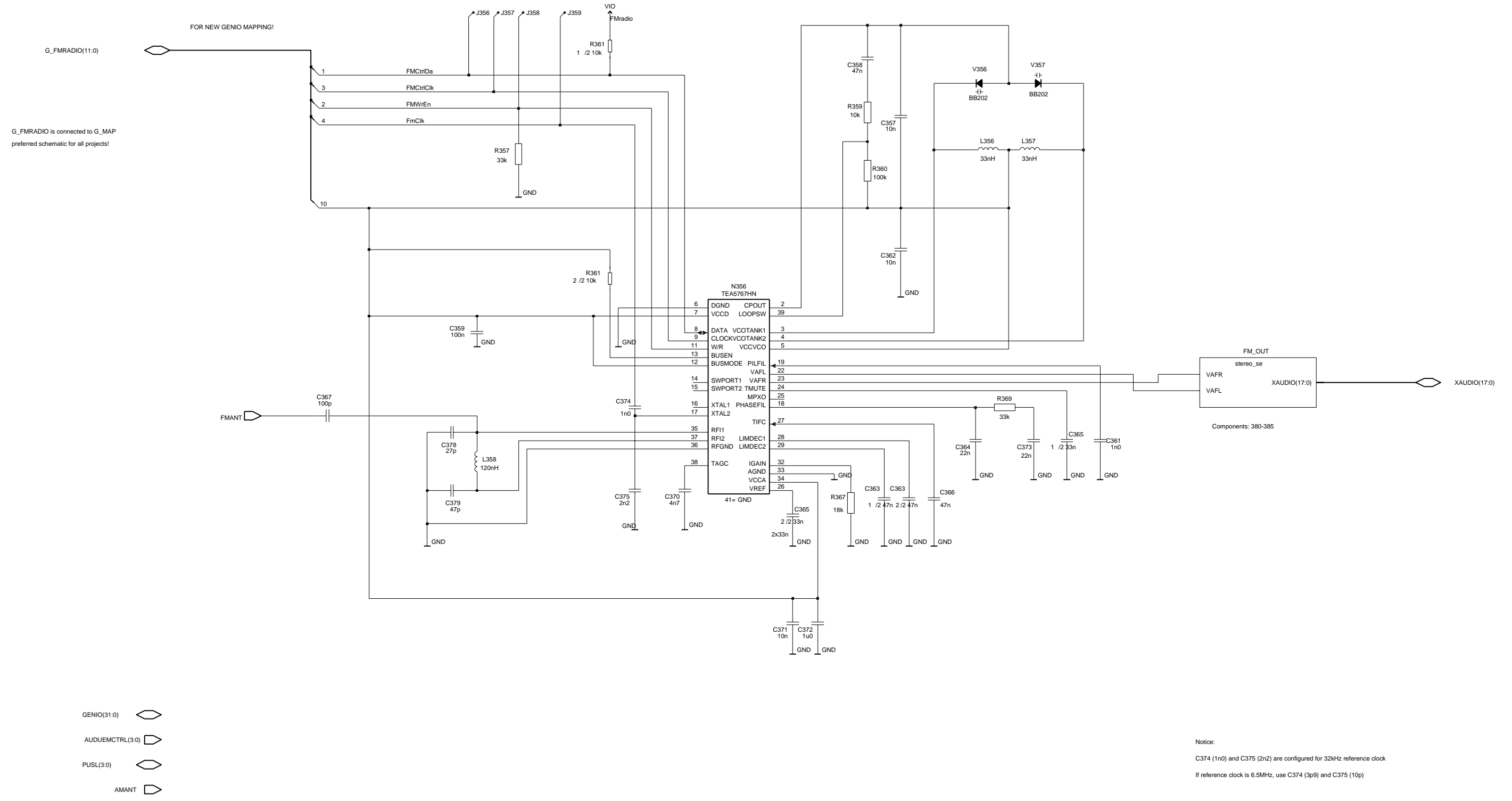


System Connector

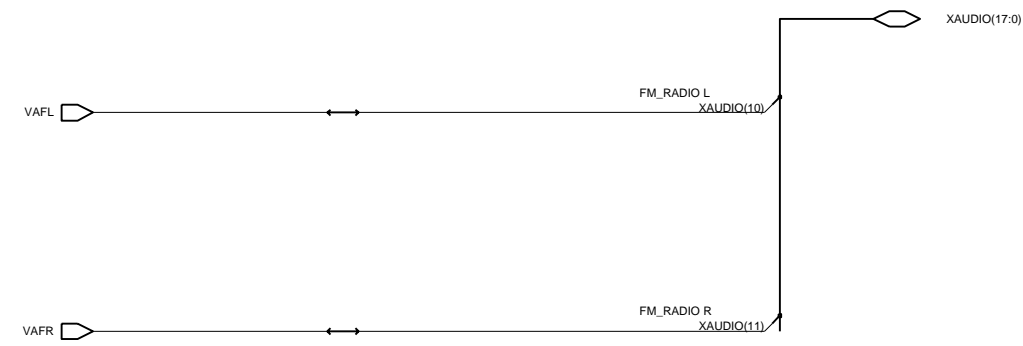


- PWRONX
- HOOKINT
- RFAUX(1:0)
- UIDRV(5:0)
- IACCDIF(5:0)
- SIMIF(3:0)
- AUDUEMCTRL(3:0)
- PUSL(3:0)
- GPIO(31:0)
- USB(2:0)
- G_SYS_CONN(11:0)
- IHF(1:0)
- APE_GPIO(15:0)
- AMANT

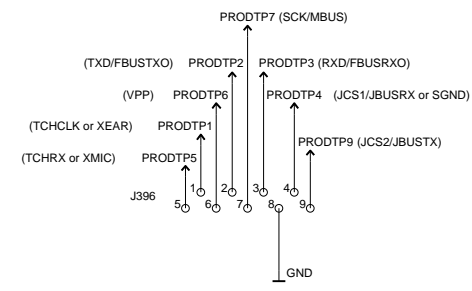
FM Radio



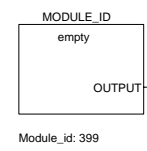
FM Radio



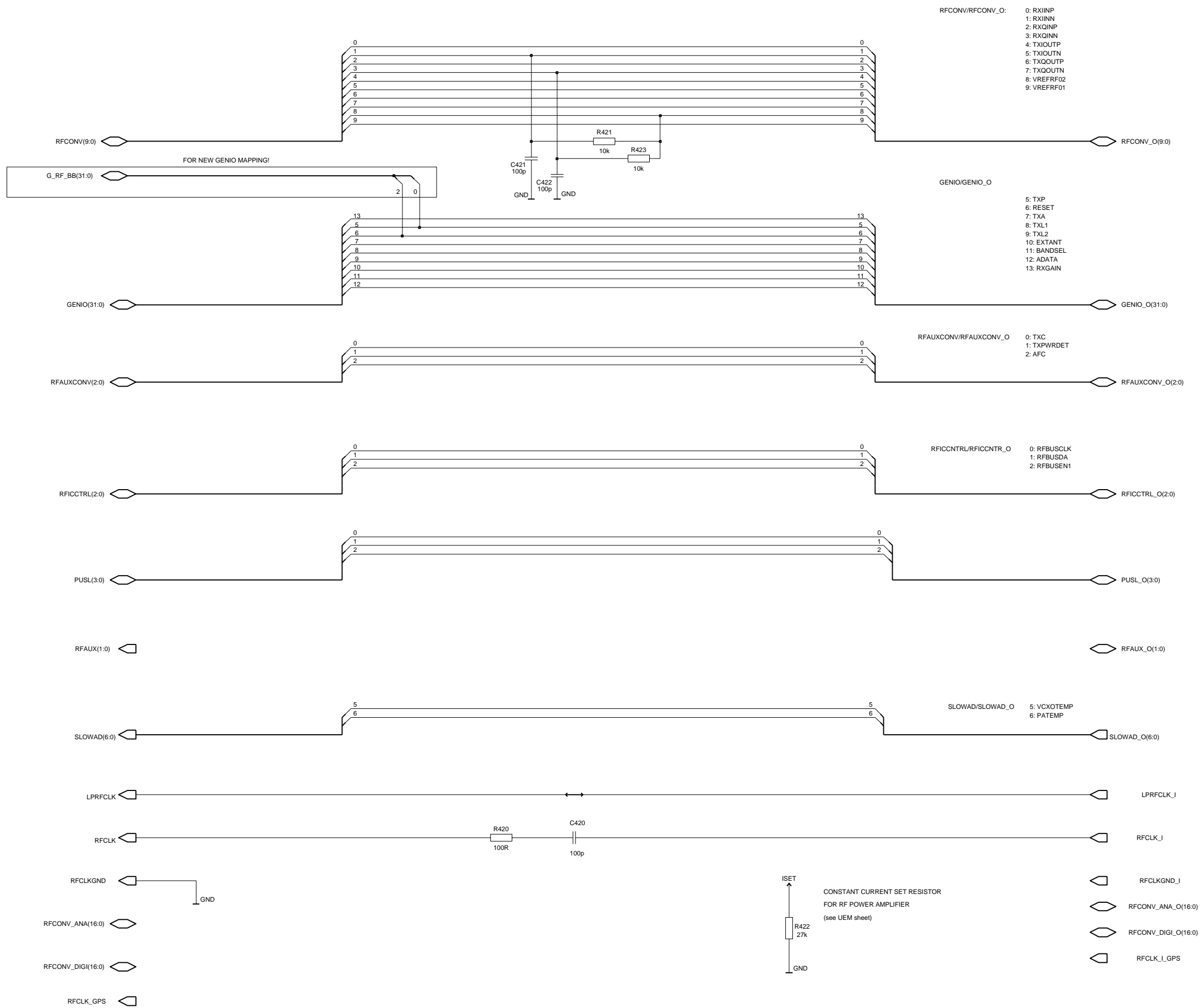
9 Pin Production Test Pattern



OUT

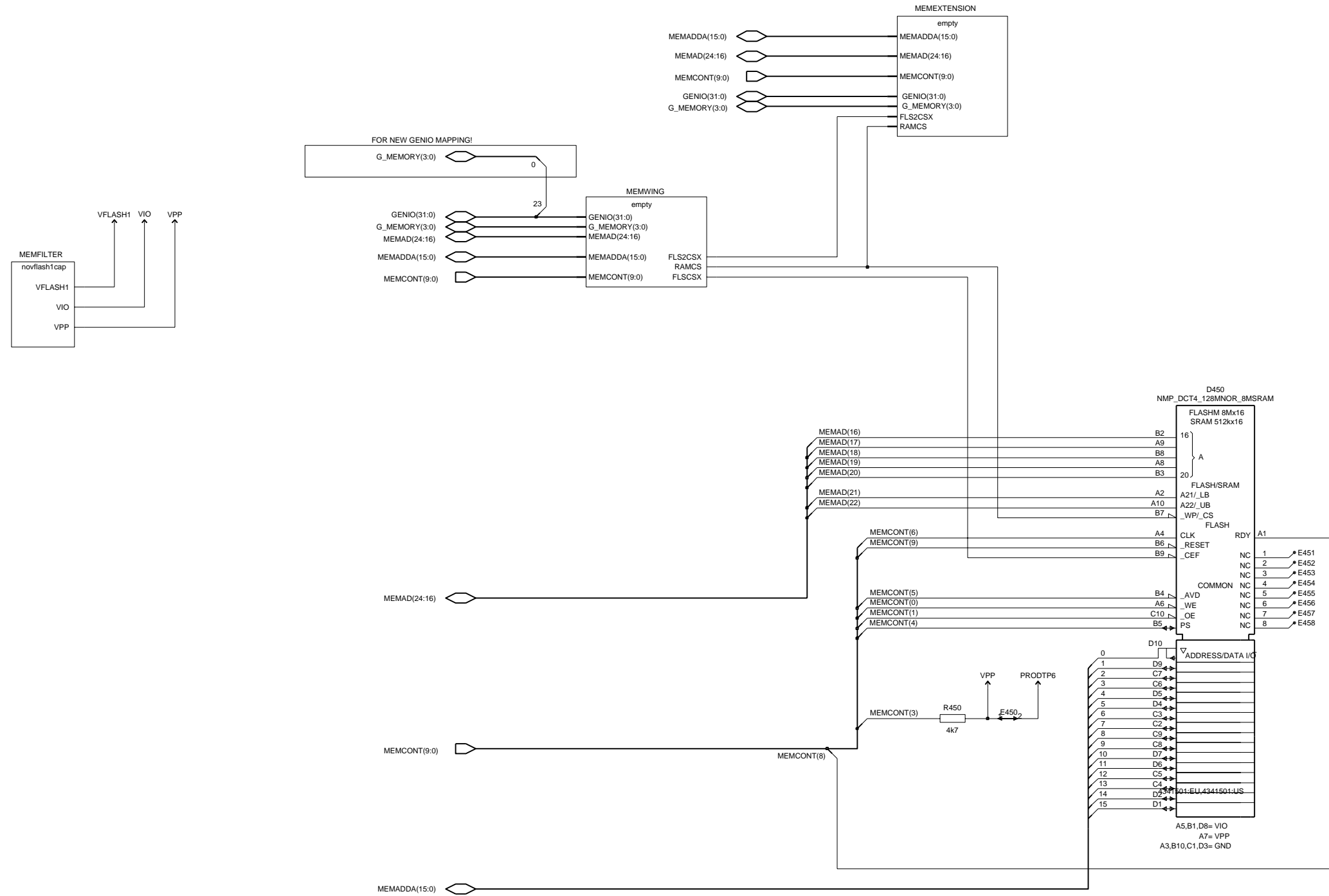


GSM RF - BB Interface

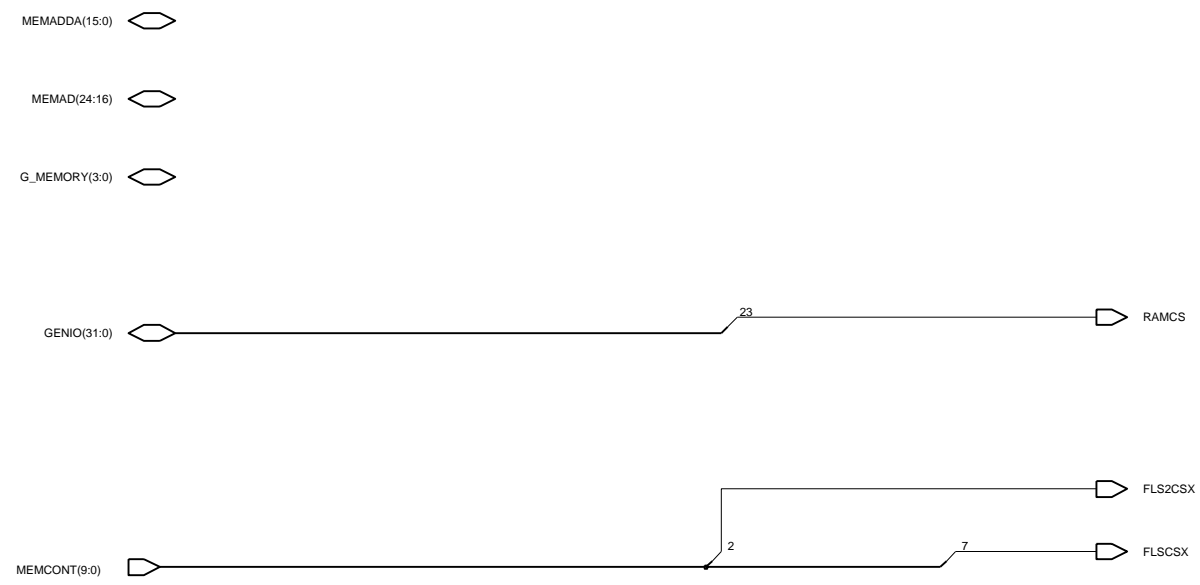


IF IPA1 AND IPA2 ARE USED BY RF THE TOLERANCE HAS TO BE 1% (0402, 1430873)

Combo Memory



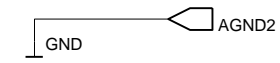
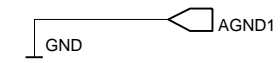
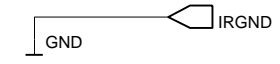
Empty Wing Sheet



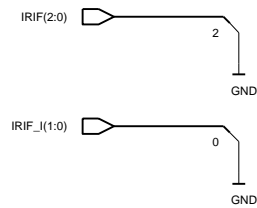
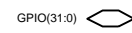
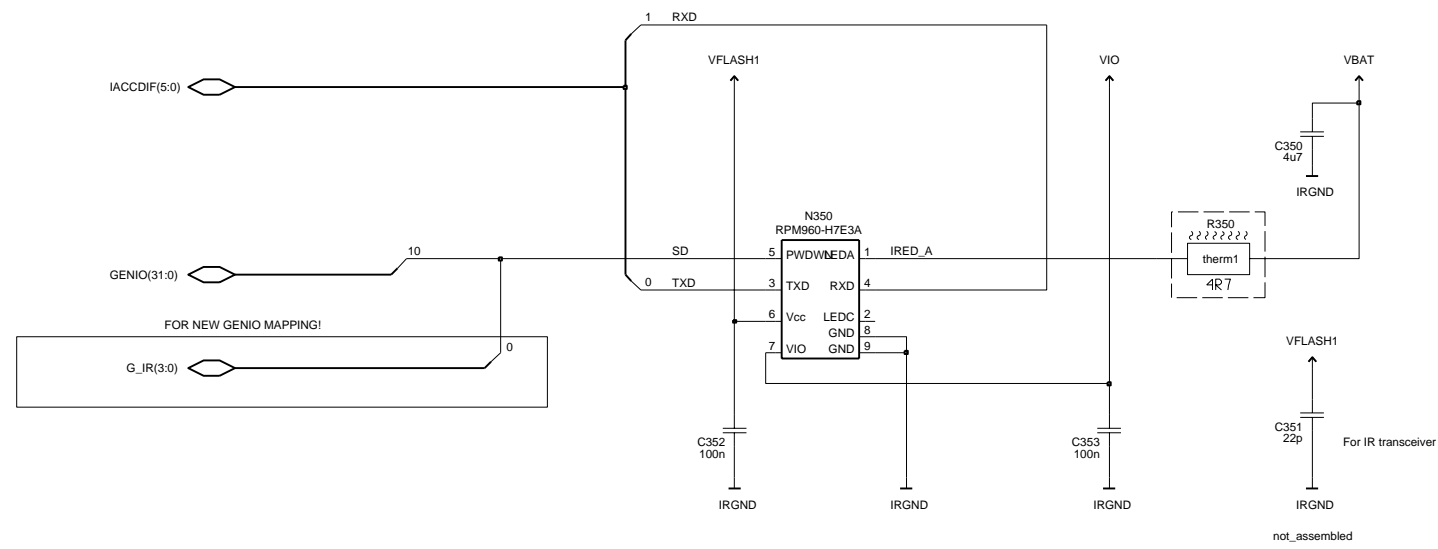
Discrete Capacitors for Memory Without VFlash1



MultiGND Symbol Bypass



IR Module



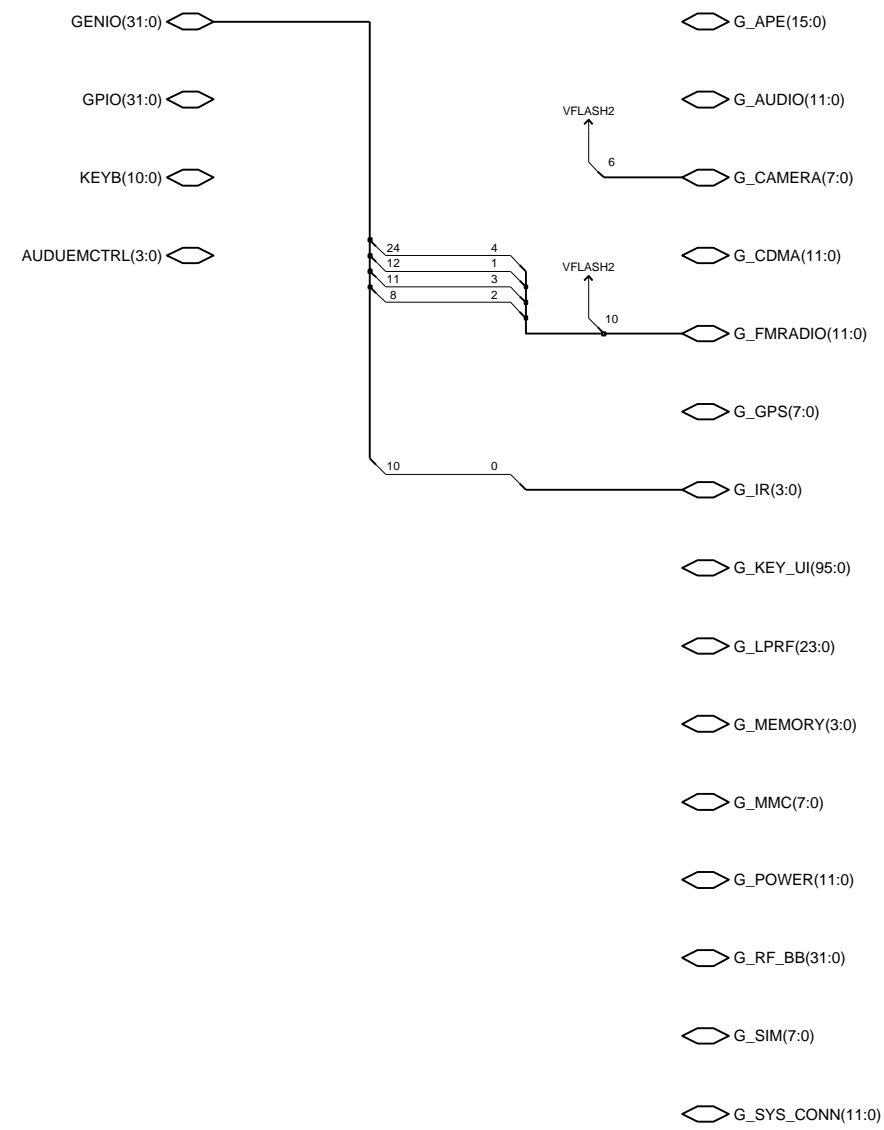
UEM IR level shifters are ground, when 1.8V IR is used!

Used references

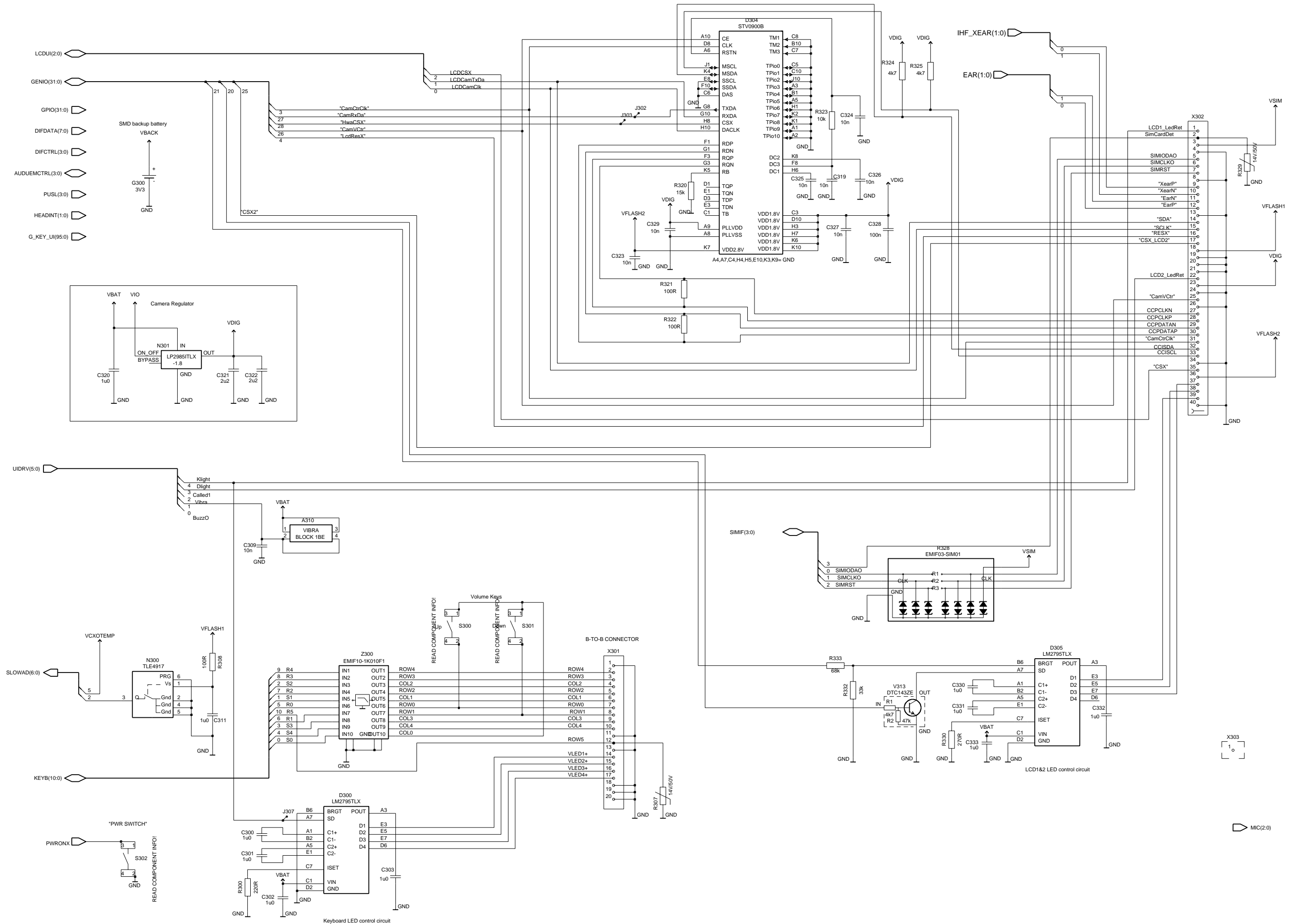
- C 350 - 353
- N 350
- R 350

GENIO and GPIO Connection Block

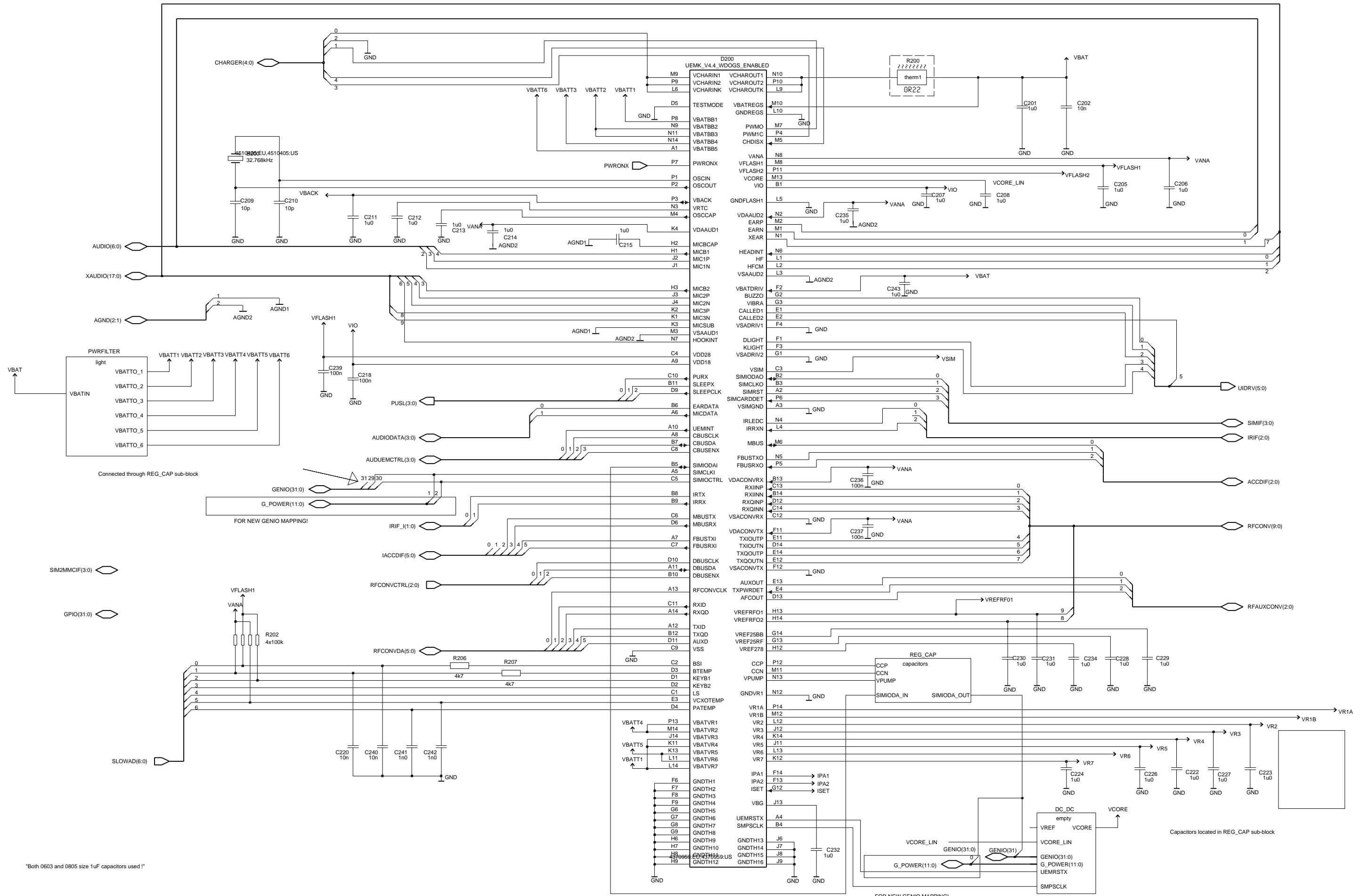
That connections used only if TEA5767 or CIT+SHARP_1.8V schematics are selected



UI



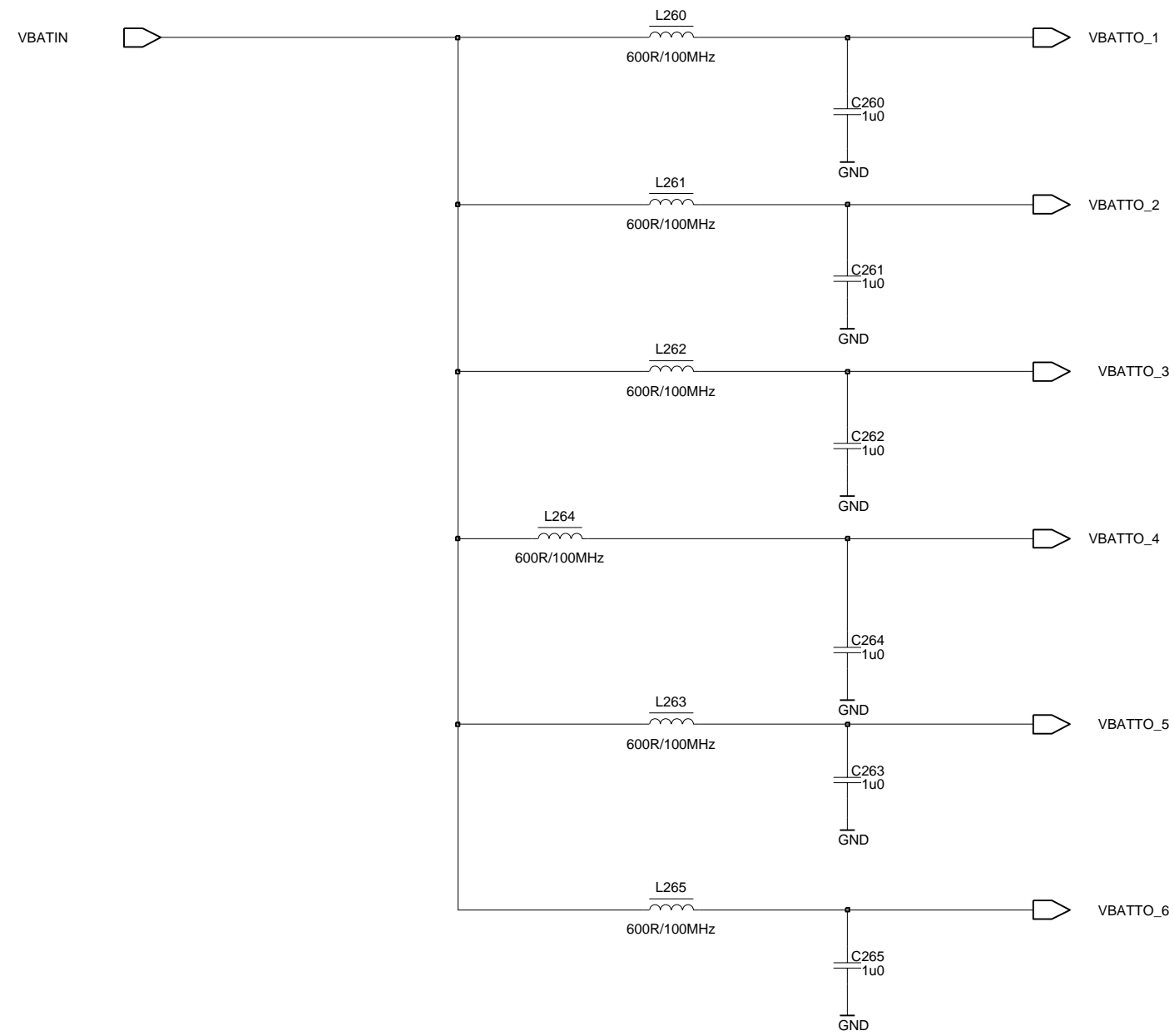
Discrete Power Management



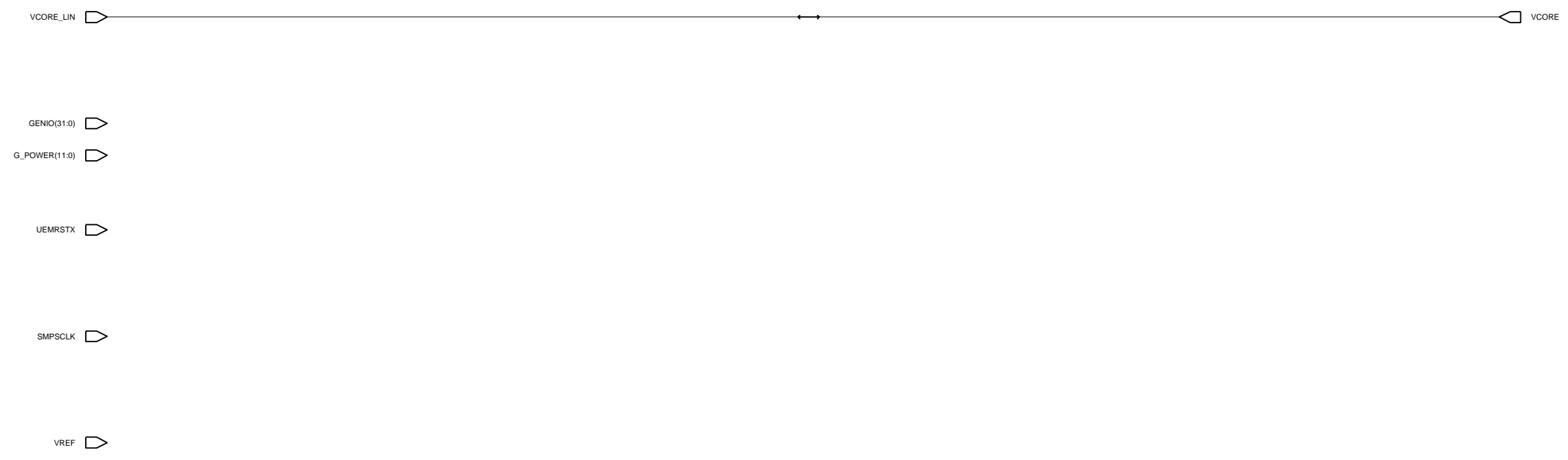
Both 0603 and 0805 size 1uF capacitors used !

Capacitors located in REG_CAP sub-block

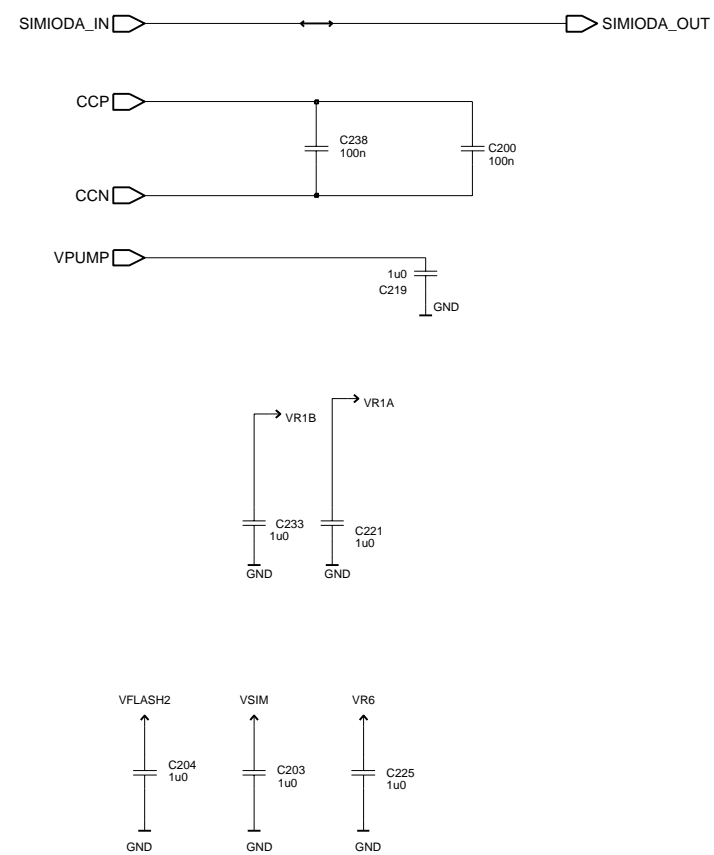
Light Filtering



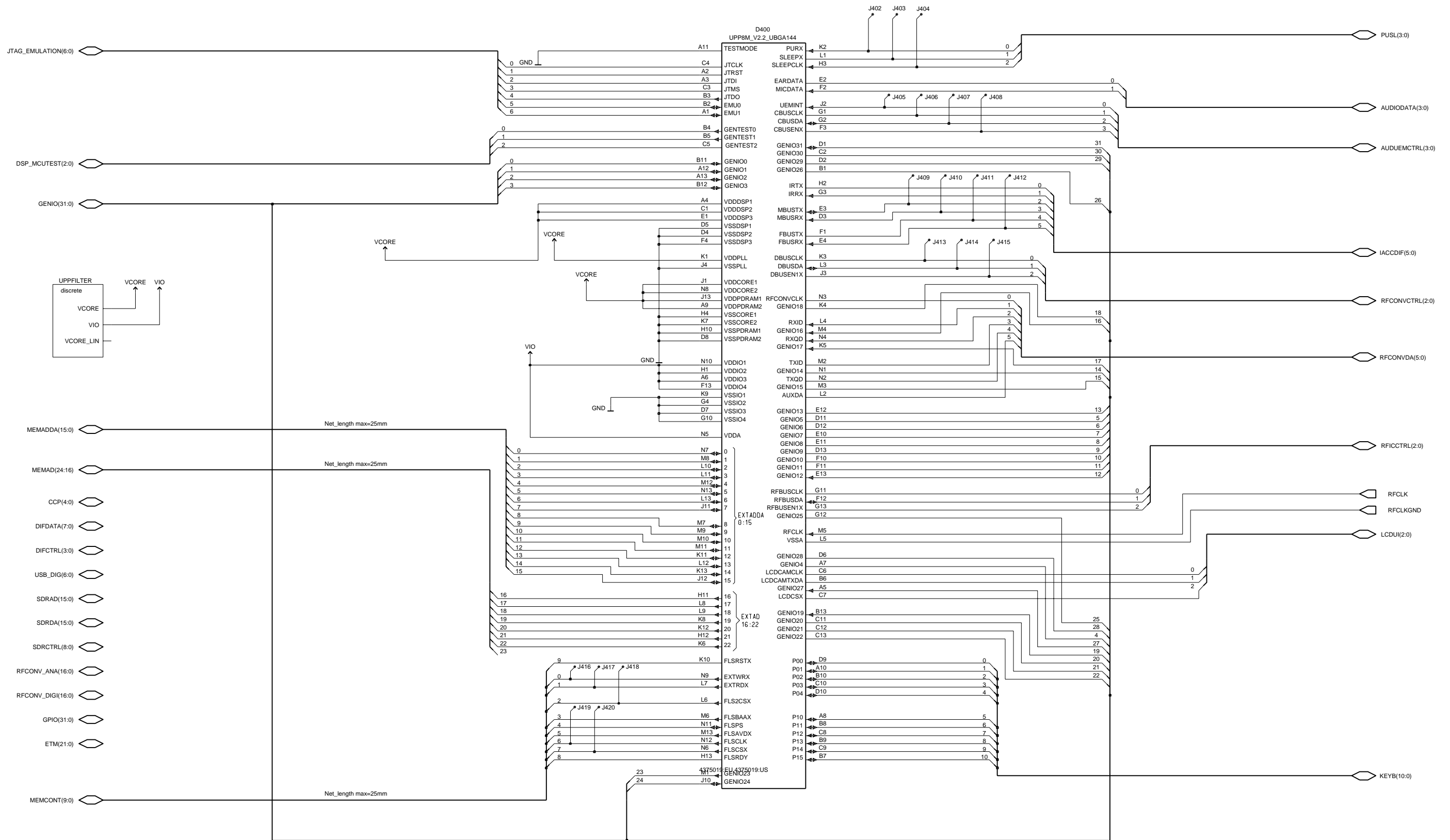
DC/DC Converter



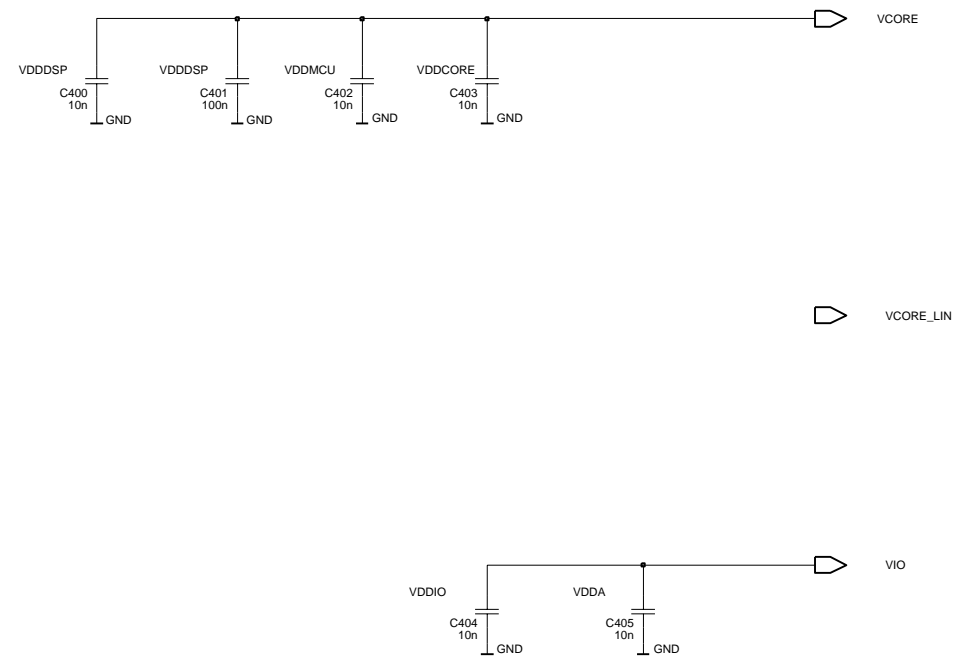
For Old Power Discrete Users



UPP 8M Implementation

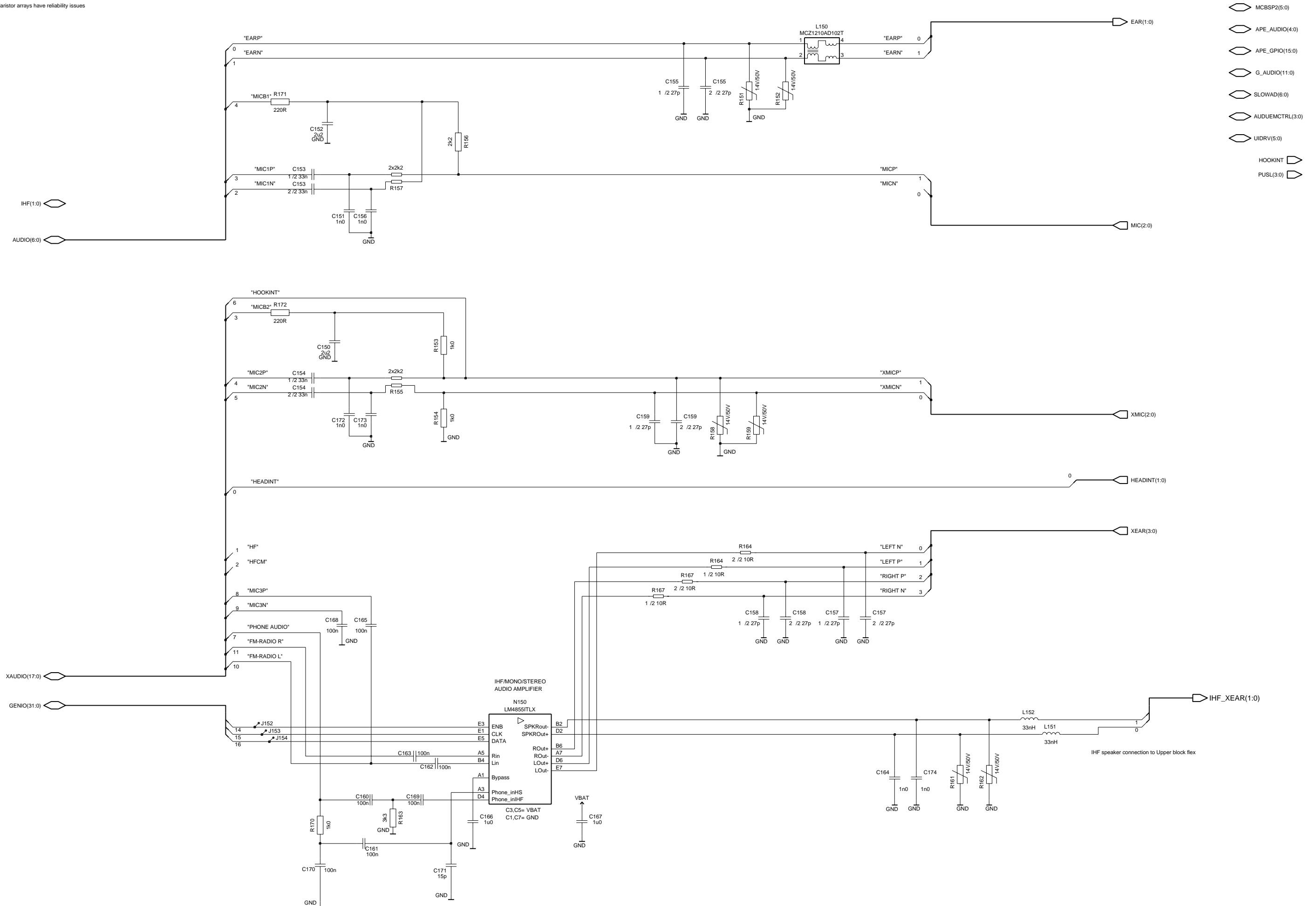


Discrete Decoupling Capacitors for UPP

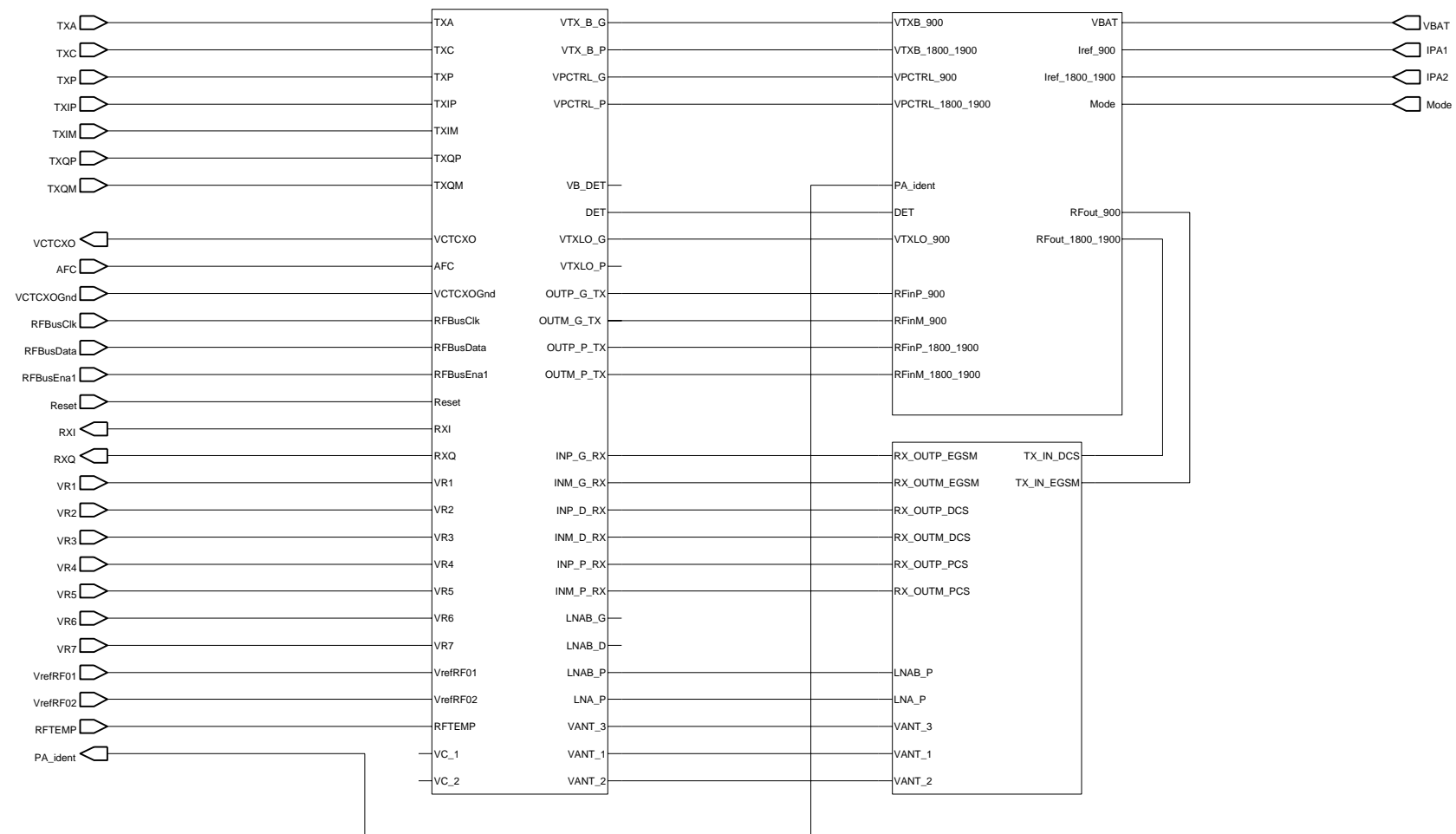


Audio

Varistor arrays have reliability issues

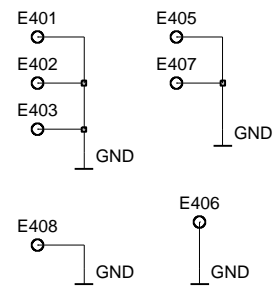


RF Top Level

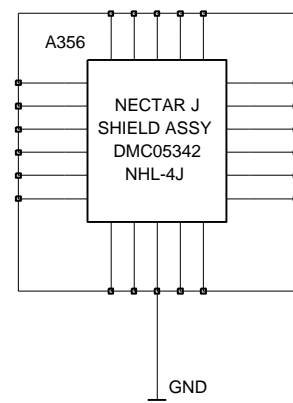


RF Shields and Vias

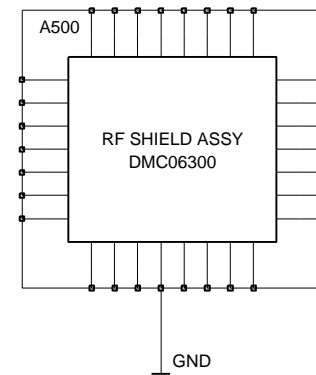
Mechanical plated through holes



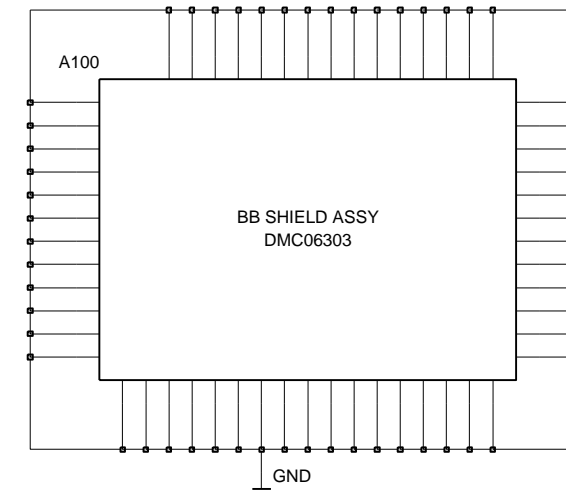
Radio shield



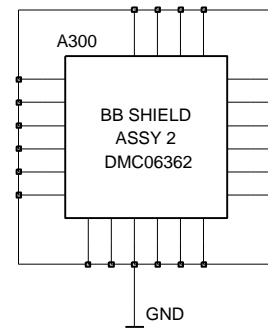
RF1 shield (PA)



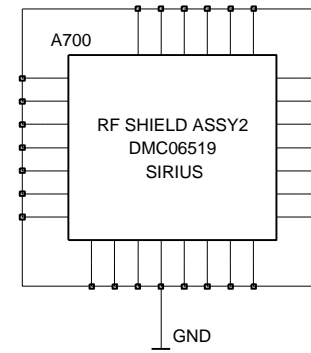
BB shield



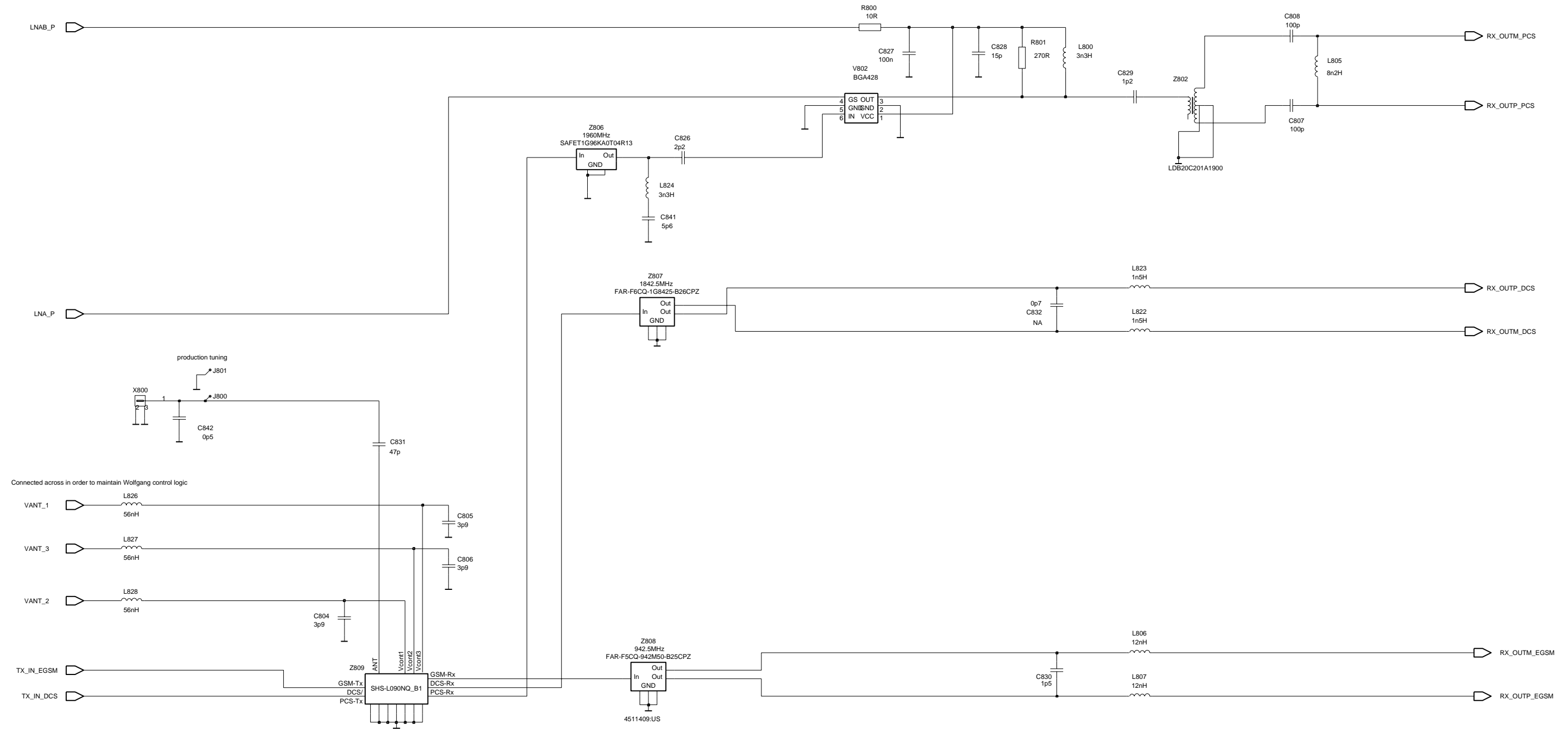
HW accelerator shield



RF2 shield (Helgo)



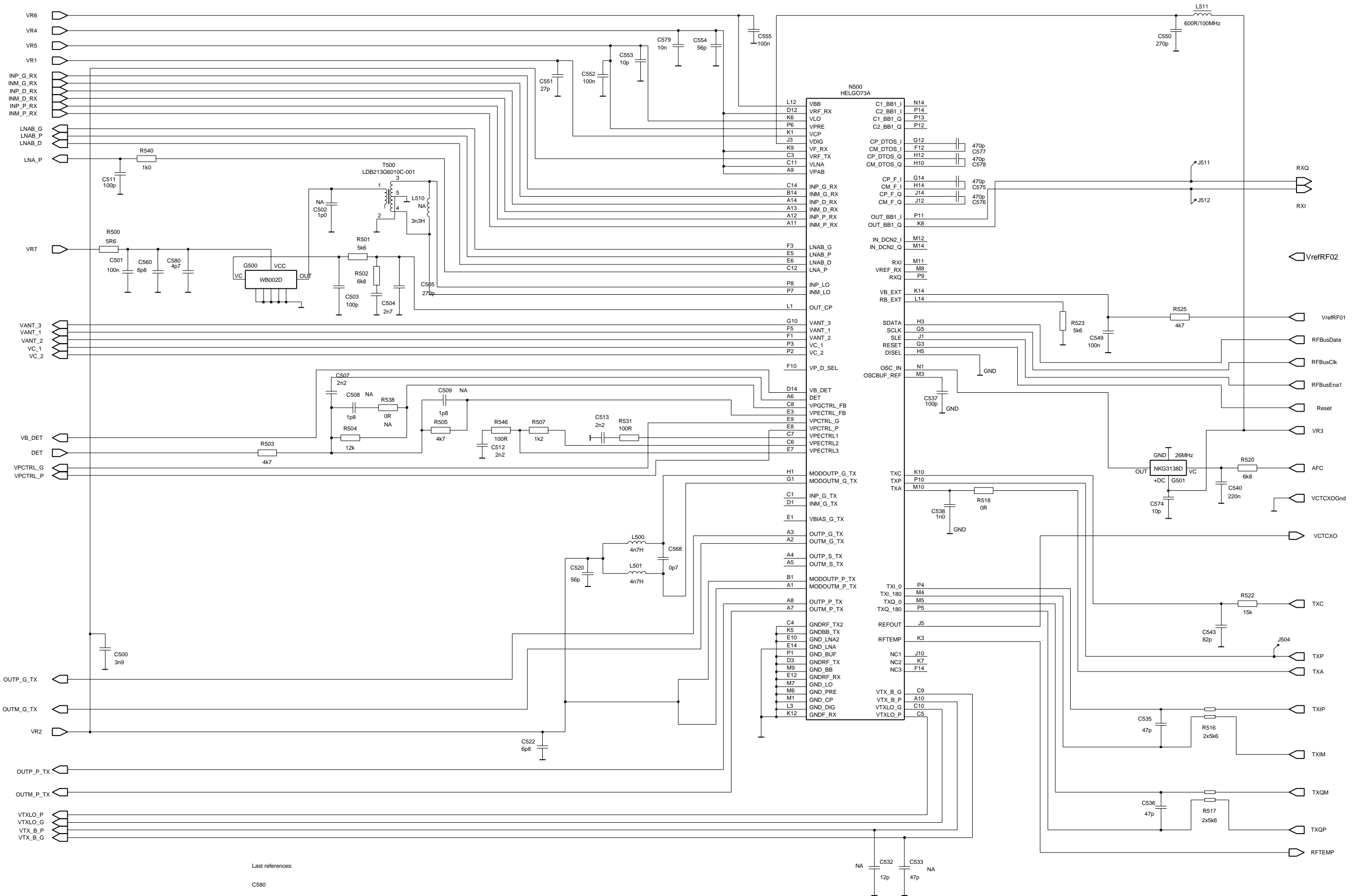
RX Front End and Antenna Switch



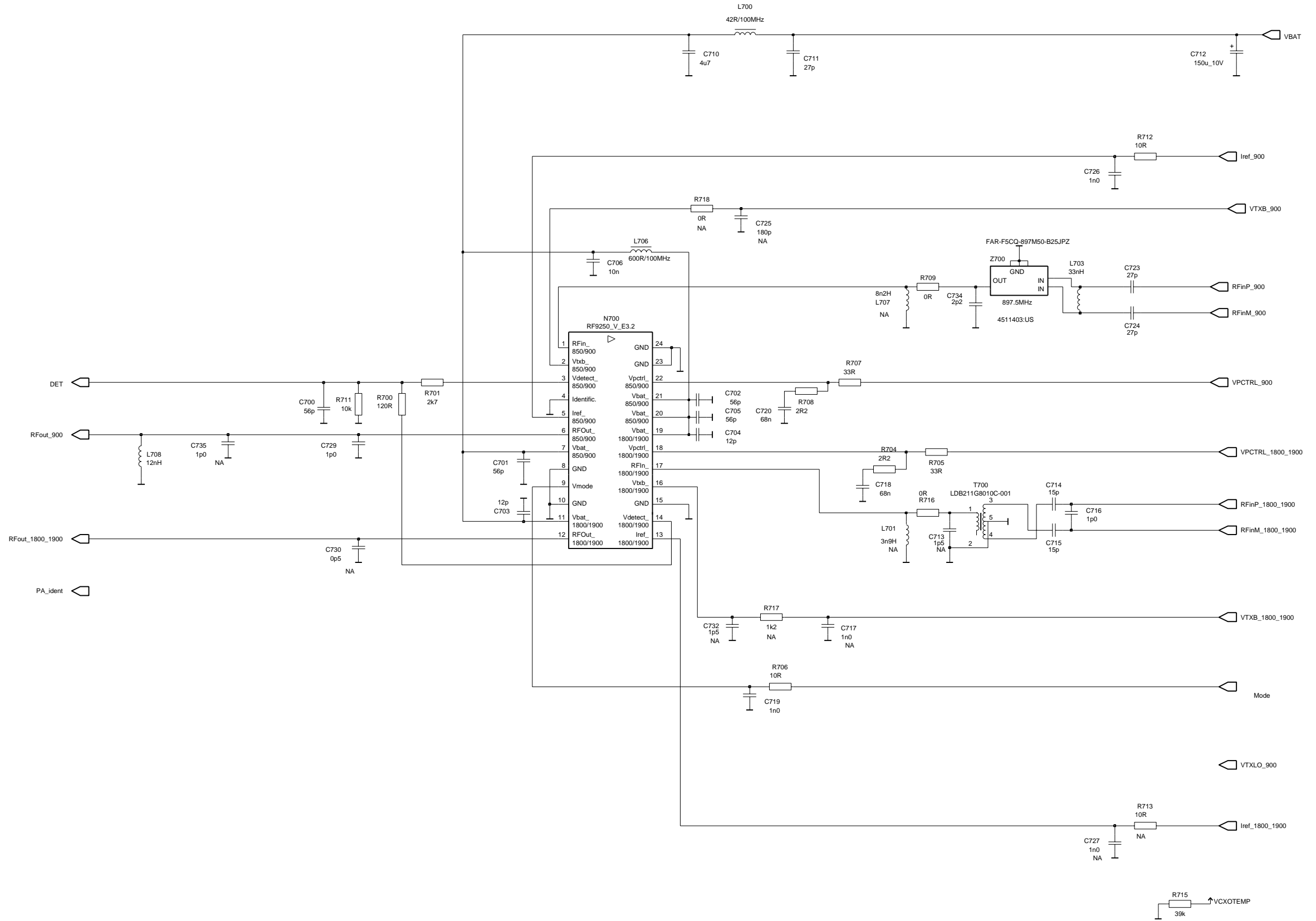
Last references:

- C842
- R807
- L830
- Z809
- V801

HELGA



TX



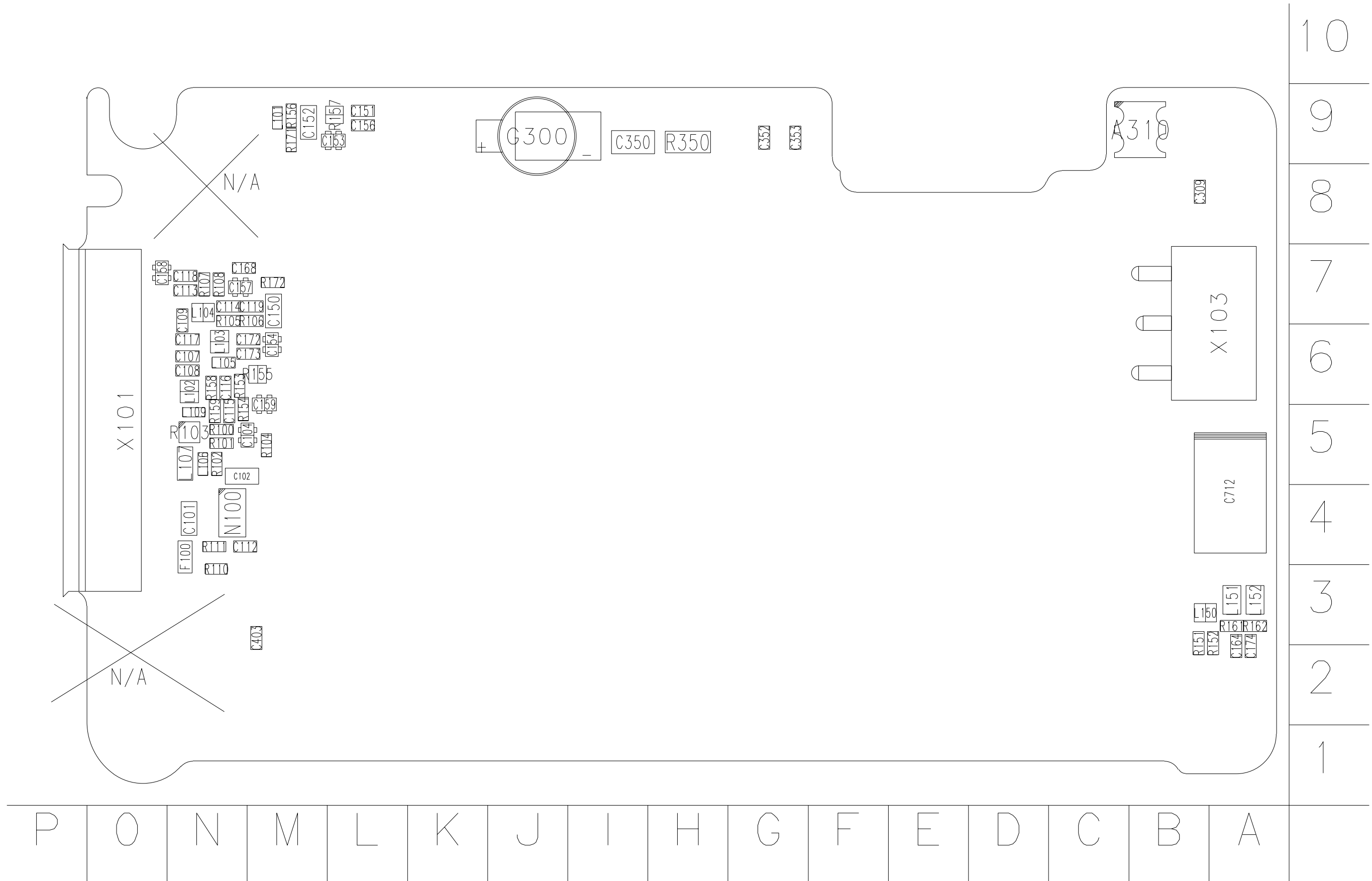
Last references:

C734

R718

L708

Component Placement Diagram Bottom



Component Placement Diagram Top

